

The analysis of the voltage switching ripple in DC-link capacitors of three-level three-phase NPC inverters, applicable to both conventional and T-type configurations (Figure 1), is presented in this ...

Analysis of output current ripple of three-phase PWM inverters with discontinuous modulation is presented. The analytical expressions for discontinuous modulation injection signal are derived. Based on these ...

The calculations are obtained considering the requirements and restrictions referring to the high (switching)-frequency dc-link voltage ripple component. Analyses have been performed ...

In this article, to mitigate the double-line frequency power ripple at dc-link caused by unbalanced grid voltage conditions, a developed virtual phase-current regulation (VPCR) method has been proposed ...

This paper concerns with the analysis and calculation of the low-frequency dc-link input current and voltage ripple harmonic components in three-phase three-level

A complete analysis of dc-link voltage ripple for three-phase two-level inverters is presented in [4], considering both switching frequency and double fundamental frequency components.

Examining a three-phase, four-wire, split capacitor inverter, in this paper, a comprehensive dc-link voltage ripple analysis with respect to switching frequency is provided for the individual capacitor ...

As the connection to dc-link of the output capacitors may lead to substantial input current ripple, two methods using resonant input filter and split output capacitors are presented to reduce this ripple.

In this paper the complete analysis of peak-to-peak output current ripple is further developed for three-phase PWM inverters, with reference to continuous symmetric PWM, either carrier-based or space ...



Three-phase inverter DC ripple frequency

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